



2186 JFW

**PATENT**  
5681-05200/P6799

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/027,359  
Filed: December 19, 2001  
Inventor:  
Fay Chong, Jr.

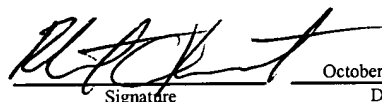
Title: Cache Accumulator  
Memory for Performing  
Operations on Block  
Operands

§ Examiner: Tsai, Sheng Jen  
§ Group/Art Unit: 2186  
§ Atty. Dkt. No: 5681-05200  
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Name of Registered Representative



Signature

October 18, 2005

Date

**RESPONSE TO OFFICE ACTION OF JULY 18, 2005**

**Mail Stop Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of July 18, 2005, to further highlight why the application is in condition for allowance.